

LISTING OF THE CLAIMS

1. (Previously Presented) A liquid crystal display device, comprising:
first and second data lines carrying data signals;
a first pixel electrode within a first pixel and spaced at a predetermined distance from the first data line; and
a second pixel electrode within a second pixel and spaced apart from the second data line by a distance different from said distance between the first data line and the first pixel electrode,
wherein a voltage deviation in the first pixel due to parasitic capacitance of the first pixel electrode is substantially the same as a voltage deviation due to parasitic capacitance of the second pixel electrode.
2. (Original) The liquid crystal display device according to claim 1, wherein the second pixel electrode has a larger dimension than the first pixel electrode.
3. (Original) The liquid crystal display device according to claim 1, wherein the first and second pixel electrodes are supplied with signals having the same polarity.
4. (Original) The liquid crystal display device according to claim 1, wherein said signals have polarity patterns inverted for each two pixel electrodes in the horizontal direction while having polarity patterns inverted for each one pixel electrode in the vertical direction.
5. (Previously Presented) A liquid crystal display device, comprising:
first and second data lines carrying data signals;
a first pixel electrode spaced at a predetermined distance from the first data line; and
a second pixel electrode spaced, apart from the second data line by a distance different from said distance between the first data line and the first pixel electrode,
wherein the second pixel electrode has a larger dimension than the first pixel electrode,
and
wherein a parasitic capacitance between the second pixel electrode and the second data line is at least three times greater than a parasitic capacitance between the first pixel electrode and the first data line.

6. (Withdrawn) A liquid crystal display device, comprising:
a first data line supplied with data; and
a second data line supplied with said data and having a different width from the first data line.

7. (Withdrawn) The liquid crystal display device according to claim 6, further comprising:
a first pixel electrode spaced at a desired distance from the first data line; and
a second pixel electrode spaced at a desired distance from the second data line.

8. (Withdrawn) The liquid crystal display device according to claim 7, wherein the first pixel electrode has the same dimension as the second pixel electrode.

9. (Withdrawn) The liquid crystal display device according to claim 7, wherein the first and second pixel electrodes are supplied with data having the same polarity.

10. (Withdrawn) The liquid crystal display device according to claim 7, wherein said data have data polarity patterns inverted for each two pixel electrodes in the horizontal direction while having data polarity patterns inverted for each one pixel electrode in the vertical direction.

11. (Withdrawn) The liquid crystal display device according to claim 7, wherein a parasitic capacitance between the second pixel electrode and the second data line has at least three times larger value than a parasitic capacitance between the first pixel electrode and the first data line.

12. (Withdrawn) A liquid crystal display device, comprising:
a first data line supplied with a signal;
a second data line supplied with said signal and parallel to the first data line; and
a protrusion electrode protruding from the second data line.

13. (Withdrawn) The liquid crystal display device according to claim 12, further comprising:

a first pixel electrode spaced at a desired distance from the first data line; and
a second pixel electrode spaced at a desired distance from the second data line and overlapped with the protrusion electrode at a portion thereof.

14. (Withdrawn) The liquid crystal display device according to claim 13, wherein the first pixel electrode has the same dimension as the second pixel electrode.

15. (Withdrawn) The liquid crystal display device according to claim 13, wherein the first and second pixel electrodes are supplied with signals having the same polarity.

16. (Withdrawn) The liquid crystal display device according to claim 13, wherein said signals have polarity inverted for each two pixel electrodes in the horizontal direction while having polarity inverted for each one pixel electrode in the vertical direction.

17. (Withdrawn) The liquid crystal display device according to claim 13, wherein a parasitic capacitance between the second pixel electrode and the second data line and a parasitic capacitance between the protrusion electrode and the second pixel electrode are at least three times greater than a parasitic capacitance between the first pixel electrode and the first data line.

18. (Withdrawn) A liquid crystal display device, comprising:
first and second data lines supplied with signals;
a first pixel electrode spaced at a predetermined distance from the first data line;
a second pixel electrode spaced at a second predetermined distance from the second data line; and
a protrusion electrode protruded from the second pixel electrode into the second data line and overlapped at a portion thereof.

19. (Withdrawn) The liquid crystal display device according to claim 18, wherein the first pixel electrode has the same dimension as the second pixel electrode.

20. (Withdrawn) The liquid crystal display device according to claim 18, wherein the first and second pixel electrodes are supplied with signals having the same polarity.

21. (Withdrawn) The liquid crystal display device according to claim 18, wherein said signals have polarity inverted for each two pixel electrodes in the horizontal direction while having polarity inverted for each one pixel electrode in the vertical direction.

22. (Withdrawn) The liquid crystal display device according to claim 18, wherein a parasitic capacitance between the second pixel electrode and the second data line and a parasitic capacitance between the protrusion electrode and the second pixel electrode are at least three times greater than a parasitic capacitance between the first pixel electrode and the first data line.